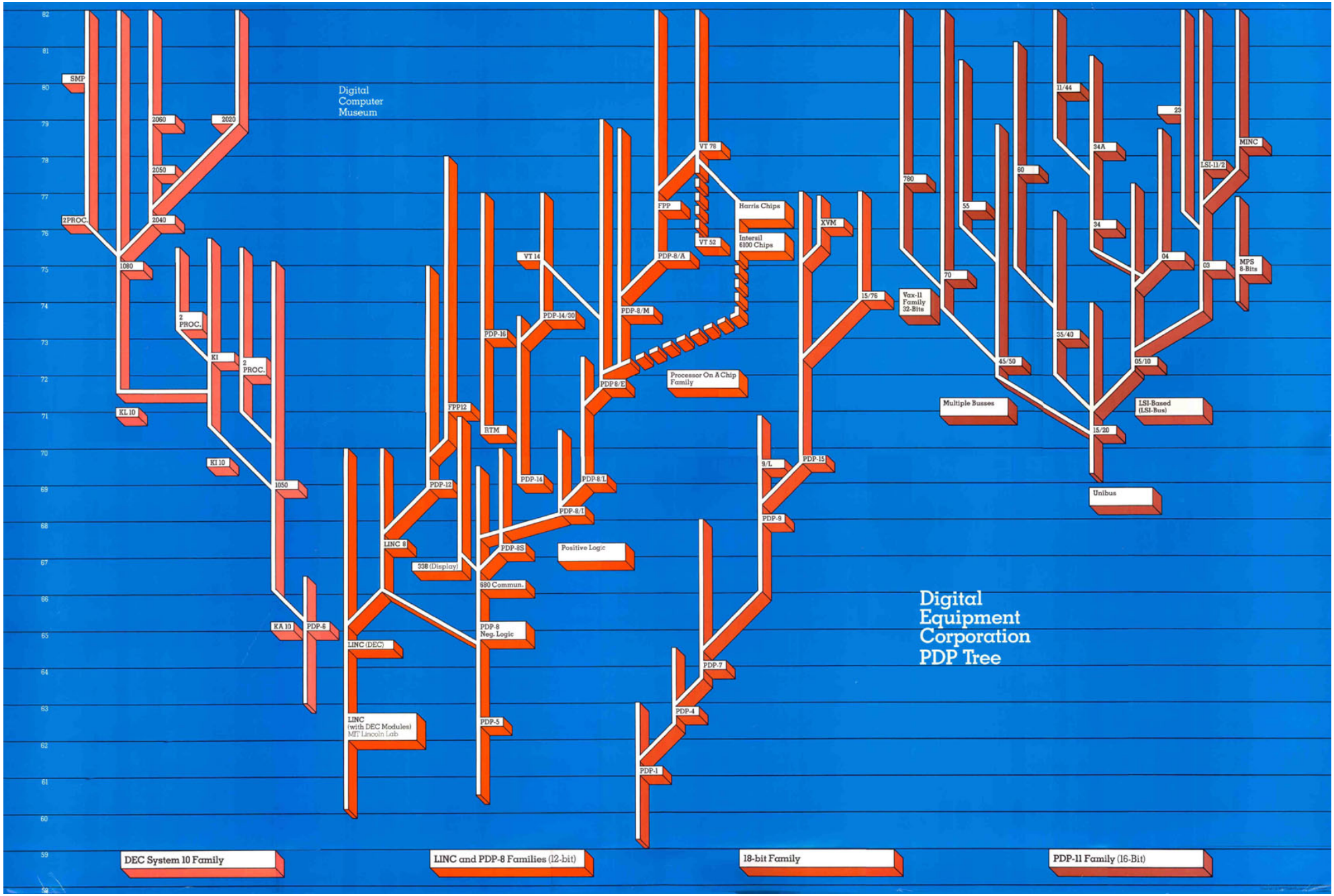
Digital Computer Museum
Digital Equipment Corporation PDP Tree
82
81
80
79
78
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64
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61
60
59
58
SMP
2PROC.
2060
2050
2040
2020
1080
KL 10
2 PROC.
KI
KI 10
2 PROC.
1050
KA 10
PDP-6
LINC (DEC)
LINC (with DEC Modules) MIT Lincoln Lab
LINC 8
338 (Display)
FPP12
PDP-12
RTM
PDP-16
PDP-8S
680 Commun.
PDP-8 Neg. Logic
PDP-5
VT 14
PDP-14/30
PDP-14
PDP-8/E
PDP-8/M
PDP-8/A
PDP-8/L
PDP-8/I
Positive Logic
FPP
VT 78
VT 52
Harris Chips
Intersil 6100 Chips
Processor On A Chip Family
PDP-1
PDP-4
PDP-7
PDP-9
9/L
PDP-15
XVM
15/76
780
Vax-11 Family 32-Bits
70
55
60
45/50
Multiple Busses
11/44
34A
34
35/40
15/20
Unibus
23
04
05/10
LSI-Based (LSI-Bus)
LSI-11/2
03
MINC
MPS 8-Bits
DEC System 10 Family
LINC and PDP-8 Families (12-bit)
18-bit Family
PDP-11 Family (16-Bit)