

RAM TYPE & TIMING

RAM	REAL NAME	USES			READ ACCESS	SPEEDS	BANDWIDTH	NOTES
		Main	Cache	Video	(Clock Cycles)	(In NS)		
PACKAGE								
SIMM	Single Inline Memory Module	X						
30 Pin		X				8 bit	8 bit - installed in pairs & quads for 16 & 32 bit CPU's	
36 Pin		X				8 bit	Rambus SIMM design	
72 Pin		X				32 bit	32 bit - installed in pairs for 64 bit data path CPU's	
DIMM	Dual Inline Memeory Module	X						
168 Pin		X				64 bit	64 bit - can be installed singularly for 64 bit CPU's	
ARCHITECTURE								
DRAM	Dynamic RAM						One transtiser & one capacitor per memory location	
FPM	Fast Page Mode DRAM	X		X	5-3-3-3	70-60	32 & 64 bit 60 ns must be used with 66 MHz system bus	
EDO	Extended Data Out DRAM	X		X	5-2-2-2	70-60-50	32 & 64 bit 50 ns can be used with Triton HX or VX chipsets	
BEDO	Burst Extended Data Out	X			5-1-1-1		32 & 64 bit Internal address counter; limited to 66 MHz bus	
SDRAM	Synchronous DRAM	X			5-1-1-1	10	32 & 64 bit Supports a 100 MHz system bus speed; VX chipset	
RDRAM	Rambus DRAM	X	X		5-1-1-1	3.75	8 bit (16) Capable of 533 MHz clock. Two channel configuration	
SRAM	Static RAM		X			12-8.5	Two transistors per memory location (off or on)	
ASRAM	Asynchronous SRAM		X			20-15-12	Not fast enough for synchronous CPU access	
SSRAM	Synchronous Burst SRAM		X		2-1-1-1		Performance declines with < 66 MHz system bus	
PB SRAM	Pipelined Burst SRAM		X		3-1-1-1	8-4.5	Good for system bus speeds of 75 to 133 MHz	
VRAM	Video RAM			X			Dual ported for separate RAM DAC & chipset access	
WRAM	Windows RAM			X			Dual ported for separate RAM DAC & chipset access	
MDRAM	Multibank DRAM			X			128 bit data path for ET6000 128 bit chipsets	
SGRAM	Synchronous Graphics RAM			X			For high speed (125 MHz+) video chipset	
Clock (MHz)	Cycle (NS)							
25	40							
33	30							
50	20							
66	15							
100	10							
200	5							

